AMENDMENTS TO THE CLAIMS

- 1. (previously presented) A system for transmitting a modulated RF carrier audio signal from a base unit to a receiver unit, the base unit having a pair of audio input connections coupled to an audio source amplification device for receiving left and right audio signals, the receiver unit having a pair of electroacoustic transducers for reproducing demodulated left and right audio signals modulated upon the RF carrier audio signal; the system comprising:
- a) a transmitting circuit located within the base unit and having a first antenna and first, second and third circuits, the pair of audio input connections of the base coupled to the first circuit, the first circuit comprising an audio signal processing circuit, the second circuit comprising a micro control unit and control circuitry and the third circuit comprising a power supply circuit and charge circuit;
- b) the audio signal processing circuit of the transmitter first circuit modulating the left and right audio signals received from the audio source amplification device for delivery upon an RF carrier signal in the 900MHz range;
- c) the micro control unit of the transmitter second circuit sending a control signal to the transmitter first circuit for choosing the RF carrier signal to be transmitted;

- d) the power supply circuit and charge circuit of the transmitter third circuit supplying a DC voltage to the transmitter;
- e) the first antenna transmitting the modulated RF carrier audio signal in the 900MHz range to the receiver;
- f) a receiver circuit located within the receiver unit and coupled to the pair of electroacoustic transducers enclosed therewithin, the receiver circuit comprising a second antenna coupled to an input network, a UHF module, a filtering network, a control unit and an audio amplifier, and
- g) the UHF module of the receiver circuit downconvertering the modulated RF carrier audio signal in the range of 900 MHz to an audio signal which is reproducible by the receiver unit electroacoustic transducers through audio amplification, said downconverting occurring only once and not requiring an intermediate carrier frequency.
- 2. (original) The system of Claim 1, wherein the audio processing circuit of the transmitting circuit first circuit comprises an auto level control amplifier circuit coupled to the pair of audio input connections, left and right audio frequency filtering and pre-emphasis circuits coupled to the auto level control amplifier circuit, a stereo multiplexer IC coupled to the left and right audio frequency filtering and

pre-emphasis circuits, a UHF module coupled to the stereo multiplexer IC, and the first antenna coupled to the UHF module.

- 3. (original) The system of Claim 1, wherein the micro control unit and control circuitry of the transmitting circuit second circuit comprises a CPU coupled to the audio processing circuit UHF module and first and second auto power circuits coupled to the CPU.
- 4. (original) The system of Claim 3, wherein the CPU comprises a micro control unit for sending a control signal to a phase lock loop circuit within the transmitting circuit UHF module.
- 5. (previously presented) The system of Claim 1, wherein the power supply circuit and charge circuit of the transmitting circuit third circuit comprises a 12V DC adapter coupled to an AC power source and a voltage regulator coupled to the 12V DC adapter for supplying a constant DC voltage to the transmitting circuit.

- 6. (original) The system of Claim 2, wherein the stereo multiplexer IC of the transmitting circuit first circuit outputs a stereo multiplexed audio modulated signal having left and right audio signals and a pilot tone signal.
- 7. (original) The system of Claim 6, wherein the pilot tone signal is 19KHz.
- 8. (original) The system of Claim 2, wherein the UHF module of the transmitting circuit first circuit outputs a 912.5MHz RF carrier signal.
- 9. (original) The system of Claim 2, wherein the first antenna transmits the modulated RF carrier signal.
- 10. (original) The system of Claim 1, wherein the auto level control amplifier circuit of the transmitting circuit first circuit is a monolithic integrated circuit having a dual equalizer amplifier.
- 11. (original) The system of Claim 1, wherein the transmitting circuit first circuit UHF module comprises a voltage controlled oscillator, a phase lock loop circuit and a radio frequency amplifier.

- 12. (previously presented) The system of Claim 1, wherein the once downconverted reproducible audio signal has a frequency of 10.7MHz.
- 13. (original) The system of Claim 1, wherein the first antenna is a one-quarter wavelength transmitting antenna.
- 14. (currently amended) A system for transmitting a modulated RF carrier audio signal from a base to a receiver unit, the base unit including a pair of audio input connections coupled to a transmitting circuit having an antenna, the pair of audio input connections receiving left and right audio signals from an audio source amplification device, the system comprising:
- a) a receiver circuit enclosed within the receiver unit coupled to a pair of electrostatic transducers for receiving the modulated RF carrier audio signal and downconverting said signal once to a second signal reproducible by the electrostatic transducers without the need of an intermediate carrier signal; and
- b) the receiver circuit having an antenna for receiving the modulated RD carrier audio signal, a single downconverter and a control circuit[.]; and
- c) the modulated RF carrier audio signal is in the range of 900MHz and the once downconverted second signal

- 15. (original) The system of Claim 14, wherein the single downconverter comprises:
 - a) a frequency mixer;
 - b) a local oscillator; and
 - c) a phase lock loop circuit.
- 16. (canceled).
- 17. (original) The system of Claim 15, wherein the local oscillator, controlled by the phase lock loop circuit, produces a desired tunable frequency signal which is subsequently directed to the downconverter frequency mixer.
- 18. (original) The system of Claim 17, wherein the desired tunable frequency signal is locked in reaction to the phase lock loop circuit receiving a feedback signal from the local oscillator and creating an error voltage.
- 19. (original) The system of Claim 15, wherein the receiver circuit control circuit produces a stable frequency signal which is used by the phase lock loop circuit as a reference frequency signal for the downconverter local oscillator.

20. (original) The system of Claim 19, wherein the reference frequency signal is adjustable by the receiver circuit control circuit to a desired tunable frequency signal, the desired tunable frequency signal enabling the local oscillator frequency signal to be matched with the modulated RF carrier audio signal in the downconverter mixer to produce the second signal reproducible by the electroacoustic transducers.